

Notice of References Cited

Application/Control No.

10/047,379

Applicant(s)/Patent Under
Reexamination
HUANG ET AL.

Examiner

Matthew A. Anderson

Art Unit

1765

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)				
	U	Wolf et al., "Silicon Processing for the VLSI Era Vol: 1 Process Technology", Lattice Press, Sunset Beach, CA, USA, pp. 143-145, 1986.				
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

Matthew A. Anderson 3/5/04